

AMENDMENTS TO THE SPECIFICATION:

Please amend the specification as follows:

On page 6, amend line 2 under the “Brief Description of the Drawings” section as follows:

~~Fig. 2 is~~ Figs. 2(a) and 2(b) are a time chart showing the relation between the voltage of each phase, PWM signal, and carrier synchronizing signal when the detection voltage in the first embodiment shown in Fig. 1 is applied.

On page 12, amend Paragraph [0031], line 19 as follows:

In Fig. 3, V_{s0} for deciding the magnitude of the detection voltage vector V_s is set to $1/2$, thus the voltage of each phase is decided. The reason is that the voltage difference between the sections 1 and 2 is defined a ~~the~~ real detection voltage vector V_s . Further, it is desirable to set V_{s0} to a small value as far as possible as long as the variation of current can be detected. Further, in this case, on the basis of the α axis of the $\alpha - \beta$ static coordinate system having the orthogonal α axis and s axis, the phase or direction is decided and the U phase is set on the α axis. Therefore, the directions of the V and W phases are directions of $2\pi/3$ and $4\pi/3$ to the α axis respectively.